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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/601,502	06/23/2003	Chengting Zhao	P16304	9693
	7590 02/21/2007 ASCHOFF, TALWALKA	EXAMINER		
50 LOCUSTAVENUE NEW CANAAN, CT 06840			CHANG, ERIC	
			ART UNIT	PAPER NUMBER
			2116	
SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
3 MO	NTHS	02/21/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

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	Application No.	Applicant(s)		
	10/601,502	ZHAO ET AL.		
Office Action Summary	Examiner	Art Unit		
	Eric Chang	2116		
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the c	correspondence address		
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute. Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).		
Status				
 Responsive to communication(s) filed on <u>01 December</u> This action is FINAL. 2b) This Since this application is in condition for allower closed in accordance with the practice under Exercise. 	action is non-final. nce except for formal matters, pro			
Disposition of Claims				
4) ☐ Claim(s) <u>1-29</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) ☐ Claim(s) <u>10-29</u> is/are allowed. 6) ☐ Claim(s) <u>1,4 and 5</u> is/are rejected. 7) ☐ Claim(s) <u>2,3 and 6-9</u> is/are objected to. 8) ☐ Claim(s) are subject to restriction and/o	wn from consideration.			
Application Papers				
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomplicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Example 11.	epted or b) objected to by the drawing(s) be held in abeyance. Se tion is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).		
Priority under 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 				
Attachment(s)				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate		

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DETAILED ACTION

1. Claims 1-29 are pending.

Claim Rejections - 35 USC § 102

- 2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 3. Claims 1 and 4-5 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by U.S. Patent 4,004,090 to Goto et al.
- 4. As to claim 1, Goto discloses a device comprising: a synchronization circuit [50] to receive a synchronization signal, the synchronization signal substantially synchronized with a data transition [col. 2, lines 12-24, and col. 3, lines 40-47], to synchronize the synchronization signal with a clock signal [11], and to generate a load signal based on the synchronized synchronization signal [col. 3, lines 44-68, and col. 4, lines 1-4]; and a ring counter [12] to receive the load signal from the synchronization circuit and to circularly propagate the load signal [col. 4, lines 1-6].
- 5. As to claim 4, Goto discloses a ring counter [12] to receive the load signal. By definition, a ring counter inherently comprises one or more delay elements to receive a signal and delay the signal and to output the delayed signal.

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6. As to claim 5, Goto discloses the clock signal reflects a clock period, and each of the one or more delay elements delays the load signal for a respective period substantially equal to an integer multiple of the clock period [col. 4, lines 5-6]. Because the ring counter counts clock pulses, each individual delay element delays the load signal by a clock period; in aggregate, the delay elements delays the load signal by an integer multiple of the clock period.

Allowable Subject Matter

- 7. Claims 2-3 and 6-9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 8. Claims 10-29 are allowed.

Response to Arguments

- 9. Applicant's arguments, see pages 10-12, filed December 1, 2006, with respect to the rejection(s) of claim(s) 1-9 under 35 U.S.C. 102(b) have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of U.S. Patent 4,004,090 to Goto et al.
- 10. Applicant's arguments, see pages 12-14, filed December 1, 2006, with respect to claims 10-29 have been fully considered and are persuasive. The rejection of claims 10-29 has been withdrawn.

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Conclusion

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eric Chang whose telephone number is (571) 272-3671. The examiner can normally be reached on M-F 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on (571) 272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

February 15, 2007 ec

